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Substitute for form 1449A/PTO

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

Complete if Known	
Application Number	09/850,057
Filing Date	May 8, 2001
First Named Inventor	James Aweya et al.
Group Art Unit	2151
Examiner Name	Unassigned
Attorney Docket Number	57983.000013

Sheet 1 of 1

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OTHER PRIOR ART -- NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No.[1]	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T[2]
MB	P1	R.Morris, "Scalable TCP Congestion Control, "*Proc. IEEE infocom 200*, pp. 1176-1183.	
MB	P2	C. Villamizar and C. Song, "High Performance TCP in ANSNET," *ACM Computer Commun,. Rev.*, Vol. 24, No. 5, Oct. 1995.	
MB	P3	R. Morris, "TCP Behavior with Many Flows," *IEEE Int'l Conf. Network Protocols*, Atlanta, Georgia, Oct. 1997.	
MB	P4	W. Stevens, "TCP Slow Start, Congestion Avoidance, Fast Retransmit, and Fast Recovery Algorithms," *IETF RFC 2001*, Jan. 1997.	
MB	P5	J. Aweya, D.Y. Montuno, M. Ouellette, "Method and Apparatus for Active Queue Management Based on Desired Queue Occupancy," U.S. Patent Application Serial No. 09/455,445, December 1999.	
MB	P6	S. Floyd and V. Jacobson, "Random Early Detection Gateways for Congestion Avoidance," *IEEE/ACM Trans. Networking*, Vol. 1, No. 4, Aug. 1993, pp. 397-413.	
MB	P7	B. Braden, et al., "Recommendation on Queue Management and Congestion Avoidance in the Internet," *IETF RFC 2309*, Apr. 1998.	
MB	P8	L. Carter et al., "Exact and Approximate Membership Testers," *Proc. 10th annual ACM Symposium on Theory of Computing*, May 1978, pp. 59-65.	
MB	P9	P.E. McKenney, "High-Speed Event Counting and Classification using a Dictionary Hash Technique," *International Conference on Parallel Processing, 1989, pp. III-71-75.*	
MB	P10	R. Jain, "A Comparision of Hashing Schemes for Address Lookup in Computer Networks," Technical Report *Dec-TR-593*, Digital Equipment Corporation, Feb. 1989.	
MB	P11	M.V. Ramakrishna et al., "Efficient Hardware Hashing Functions for High Performance Computers," *IEEE Trans. Computers*, Vol. 46, No. 12, Dec 1997, pp. 1378-1381.	
MB	P12	G.F. Franklin, J.D. Powell and A. Emami-Naeini, *Feedback Control of Dynamic Systems*, Addison-Wesley Reading, Massachusetts, 1994.	
	P13		
	P14		

Examiner Signature	Majid Barenhbat	Date Considered	11/25/10 4

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[1] Unique citation designation number. [2] Applicant is to place a check mark here if English language Translation is attached.

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